

COMBINED DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled DISCRETE-TIME SAMPLING OF DATA FOR USE IN SWITCHING REGULATORS, the specification of which

☒ is attached hereto.

☐ was filed on _____ as Application Serial No. _____
and was amended on _____.

☐ was described and claimed in PCT International Application No. _____
filed on _____ and as amended under
PCT Article 19 on _____.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose all information I know to be material to patentability in accordance with Title 37, Code of Federal Regulations, §1.56.

I hereby appoint the following attorneys and/or agents to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith: David J. Goren, Reg. No. 34,609; Roger S. Borovoy, Reg. No. 20,193; Hans R. Troesch, Reg. No. 36,950; John F. Land, Reg. No. 29,554; Mark D. Kirkland, Reg. No. 40,048; John D. Cowart, Reg. No. 38,415 and John C. Phillips, Reg. No. 35,322.

Address all telephone calls to David J. Goren at telephone number 415/322-5070.

Address all correspondence to David J. Goren, Fish & Richardson P.C., 2200 Sand Hill Road, Suite 100, Menlo Park, CA 94025.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patents issued thereon.

Full Name of Inventor: Anthony J. Stratakis

Inventor's Signature:  Date: 12/16/97

Residence Address: Fremont, California

Citizen of: U.S.A.

Post Office Address: 39241 Red Hawk Terrace, Apt. B201, Fremont, CA 94538

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COMBINED DECLARATION AND POWER OF ATTORNEY CONTINUED

Full Name of Inventor: David B. Lidsky

Inventor's Signature: *David B. Lidsky*

Date: 12/10/97

Residence Address: Oakland, California

Citizen of: U.S.A.

Post Office Address: 5739 ^{Colton} Bolton Boulevard, Oakland, CA 94611

Full Name of Inventor: William A. Clark

Inventor's Signature: *William A. Clark*

Date: 12-10-97

Residence Address: Fremont, California

Citizen of: U.S.A.

Post Office Address: 63524 Terrace Drive, Fremont, CA 94526

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Applicant or Patentee: Stratakos et al.

Serial or Patent No.:

Filed or Issued:

For:

DISCRETE-TIME SAMPLING OF DATA FOR USE IN SWITCHING REGULATORS

**VERIFIED STATEMENT (DECLARATION) CLAIMING SMALL ENTITY STATUS
(37 CFR 1.9(f) and 1.27(c)) - SMALL BUSINESS CONCERN**

I hereby declare that I am

- ☐ the owner of the small business concern identified below:
☒ an official of the small business concern empowered to act on behalf of the concern identified below:

Name of Small Business Concern: Volterra Semiconductor CorporationAddress of Small Business Concern: 42840 Christy Street, Suite 202, Fremont, CA 94538

I hereby declare that the above identified small business concern qualifies as a small business concern as defined in 13 CFR 121.12, and reproduced in 37 CFR 1.9(d), for purposes of paying reduced fees to the United States Patent and Trademark Office, in that the number of employees of the concern, including those of its affiliates, does not exceed 500 persons. For purposes of this statement, (1) the number of employees of the business concern is the average over the previous fiscal year of the concern of the persons employed on a full-time, part-time or temporary basis during each of the pay periods of the fiscal year, and (2) concerns are affiliates of each other when either, directly or indirectly, one concern controls or has the power to control the other, or a third party or parties controls or has the power to control both.

I hereby declare that rights under contract or law have been conveyed to and remain with the small business concern identified above with regard to the invention, entitled DISCRETE-TIME SAMPLING OF DATA FOR USE IN SWITCHING REGULATORS by inventor(s) Anthony J. Stratakos, David B. Lidsky and Bill Clark described in

- ☒ the specification filed herewith.
☐ application serial no. , filed .
☐ patent no. , issued .

If the rights held by the above identified small business concern are not exclusive, each individual, concern or organization having rights to the invention is listed below and no rights to the invention are held by any person, other than the inventor, who would not qualify as an independent inventor under 37 CFR 1.9(c) if that person made the invention, or by any concern which would not qualify as a small business concern under 37 CFR 1.9(d), or a nonprofit organization under 37 CFR 1.9(e). NOTE: Separate verified statements are required from each named person, concern or organization having rights to the invention averring to their status as small entities. (37 CFR 1.27)

Full Name: Volterra Semiconductor CorporationAddress: 42840 Christy Street, Suite 202, Fremont, CA 94538

☐ INDIVIDUAL ☒ SMALL BUSINESS CONCERN ☐ NONPROFIT ORGANIZATION

I acknowledge the duty to file, in this application or patent, notification of any change in status resulting in loss of entitlement to small entity status prior to paying, or at the time of paying, the earliest of the issue fee or any maintenance fee due after the date on which status as a small entity is no longer appropriate. (37 CFR 1.28(b))

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application, any patent issuing thereon, or any patent on which this verified statement is directed.

Name: Anthony J. StratakosTitle: Vice-President, Chief Technical OfficerAddress: Volterra Semiconductor Corporation, 42840 Christy Street, Suite 202, Fremont, CA 94538Signature: Date: 12/10/97